

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Seymour

Docket No:

TI-35905

Serial No:

10/689,825

Examiner:

Lauture, Joseph J.

Filed:

10/20/2003

Art Unit:

2819

For:

METHOD AND CIRCUIT FOR GAIN AND/OR OFFSET CORRECTION IN

A CAPACITOR DIGITAL-TO-ANALOG CONVERTER

AMENDMENT UNDER 37 C.F.R. § 1.115

Assistant Commissioner For Patents Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

Tommie Chambers

Dear Sir:

Responsive to the Office Action mailed September 21, 2004 in connection with the above identified application, Applicants respectfully submit the following remarks.